



**CALIFORNIA STATE SCIENCE FAIR
2002 PROJECT SUMMARY**

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Project Title Take a R.I.S.C.	
<p style="text-align: center;">Abstract</p> <p>Objectives/Goals We wanted to see if we could build a RISC (Recuded Instruction Set Computer), which is in the realm of current research.</p> <p>Methods/Materials We used many standard and advanced Boolean Algebra techniques. It was critical that we researched each process for best results. We used microchips such as: adders, inverters, clocks, and tristate buffers; wires, and a proto-board.</p> <p>Results We could build a Reduced Instruction Set Computer. In many stages of our debugging we had very low accuracy rates. We fixed these problems by reseaching more efficient ways of processing commands and eliminating all processes that were not needed.</p> <p>Conclusions/Discussion We concluded that this can actually be done and is a legitimate and efficient way processing data.</p>	
Summary Statement Our project was about building a true R.I.S.C. processor.	
Help Received	